Design of MEMS Oscillator for High Frequency Application

G.Divya, M.Vinothkumar
ECE Department SRM University, Delhi-NCR

Abstract: MEMS Oscillator design for high frequency application is approached in this project and its gain, noise and power are analyzed. The CMOS Micro Electro Mechanical System resonator structure is modeled using its RLC equivalent circuit where all passive components are extracted by COMSOL Muti physics tool. MEMS based oscillator requires to be combined with amplifier circuits to improve the performance. The proposed CMOS-MEMS oscillator is capable of generating 19.8 MHz clocks. The beam resonators require approximately 35 VDC and 400mV, VAC to vibrate. The operational amplifier achieves a gain of 26dB at VDD of 1.8v and VSS of -1.8v. For this project, the MEMS based Oscillator circuit was elected due to its quality and high frequency stability. The Micro Electro Mechanical System beam resonator translates to motional resistance of $R_m=2.8M\Omega$, capacitance, $C_m=2.46aF$ and inductance, $L_m=13.6H$ respectively. The MEMS based Oscillator was simulated and measured using Cadence virtuoso to obtain the desired design parameters.

Keywords: MEMS resonator, two stage Op-amp, gain, Cadence virtuoso, COMSOL Muti physics.

I. Introduction

Resonators based on MEMS (Micro Electro Mechanical Systems) span a broad spectrum of important current applications, including detection of chemical and biological substances, measurement of rheological properties of fluids, and energy harvesting, to name only a few. While the devices that perform these diverse functions span an equally broad range in geometric layout, material properties, circuitry, fabrication techniques, packaging, and so on, they all have one aspect in common: the phenomenon of “resonance” forms the basis of their operating principle. More specifically, they usually perform their desired functions by monitoring how interactions with the environment influence the resonant behavior of the device. Conversely, how effectively the device performs its function will depend to a large degree on the underlying resonant characteristics of the device. Since all such devices rely on resonant vibrations to accomplish one or more tasks effectively, a firm understanding of this highly interdisciplinary field requires that one be familiar with the fundamental theory of mechanical vibrations. This paper presents a fixed-fixed beam MEMS Resonator. The resonating beam is fixed at both ends, called Anchor that is why called fixed-fixed Resonator. In this paper, fixed-fixed beam resonator is modeled by COMSOL Multi physics software. A lot of modes of vibration can be found in a rectangular plate resonator either by changing the thickness of electrodes or by changing the mode number. MEMS resonators are small electromechanical structures that vibrate at high frequencies. From the industrial perspective, these technologies promise to deliver exactly to their applications needs. The resulting achievable goals are devices in the form of high-capability with more embedded functional features, bearing the attributes of portability and wear-ability. MEMS are widely used for diverse applications in high (3–30MHz), very high (30–300MHz) and even ultra-high (300MHz–3GHz) frequency systems (Leach, 2001). These efforts gear up tuning into a host of devices which make a promising alternative to traditional electronic components in the design of multiple RF devices (e.g. mixers, tunable capacitors, inductors, switches, oscillators). The paper is organized as follows. Section II presents the design theory of MEMS Beam Resonator, two stage operational amplifier, oscillator including equation and circuit diagram. Section III provides simulations results of two stage amplifier.

II. Design Theory

A. Mems Beam Resonator

Fig. 1 shows the MEMS beam resonator structure. The polysilicon layers are used to define the MEMS resonator and electrodes. Here we have two polysilicon levels.

![Fig. 1: MEMS Beam Resonator](image)
One level was used for the resonator while the other polysilicon level was used for electrodes. When AC voltage is applied to the electrode, DC voltage is applied to the anchor and an, producing the microstructure being driven into resonance. The resonators have a setup configuration with separate excitation electrode and output electrodes. The beam is hanged so that it can vibrate freely under the influence electrode applied under given DC condition. The applied AC voltage results in an electrostatic force between the electrode and the beam that results into consequently vibration of the resonator. In this case, the silicon dioxide layer between the two polysilicon levels defines the lateral gap between the electrode and beam resonator, allowing gaps as narrow as 0.5μm to be realized. Polysilicon thickness, which is typically a few 1μm and therefore much thinner than the polysilicon resonator and its resonance frequency can be defined as,

\[ f_0 = 1.03 \sqrt{\frac{E W}{\rho L^2}} \]

Where,
- E: Young’s modulus of the material
- \( \rho \): Mass density of the material
- W: Beam width
- L: Beam length.

![Fig. 2: Electrical simulation setup for MEMS Beam Resonator](image)

According to the equation for resonance frequency the frequency increases as the length decreases. This oscillation of the beam changes the gap between the electrodes also changing the capacitance between the two electrodes. The variation in capacitance is maximum at \( f_0 \) and hence the current. Practically a mechanical transducer in the form of \( \lambda/4 \) length is used to connect two such beams, the ac input signal is applied to moving electrode of first beam and the output current is measured at the moving electrode of the second beam. Hence, they are resulting in an electrostatic force between the electrode and the beam that consequently causes the vibration of the resonator. The capacitance change between the sense electrode and the beam due to the movement of the beam generates a current in the sense electrode. Table 1 shows all the design parameters of Beam resonator in this work. The natural oscillation frequency depends on the shape, physical dimension of the device, the mechanical properties of the material and the vibration mode.

<table>
<thead>
<tr>
<th>Table 1: Design Parameter of MEMS Resonator</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Input Parameter</strong></td>
</tr>
<tr>
<td>Beam length, L</td>
</tr>
<tr>
<td>Beam width, W</td>
</tr>
<tr>
<td>Electrode width, W_e</td>
</tr>
<tr>
<td>Beam thickness, h</td>
</tr>
<tr>
<td>Electrode to resonator gap</td>
</tr>
<tr>
<td>Poisson Ratio, v</td>
</tr>
<tr>
<td>Young Modulus, E</td>
</tr>
<tr>
<td>Density of poly silicon, ( \rho )</td>
</tr>
</tbody>
</table>

**B. Equivalent Circuit Model**

We make a few qualitative observations. First, the beam ends move in the opposite direction and the beam is acnored from the middle which is the vibrational nodal point. Secondly, the beam is actuated symmetrically using both electrodes and is listened from the center anchor making the resonator a two port device. Applying voltage over the resonator results in ac-current flowing through the device. This current can be divided into two parts: “normal” current \( i_{ac} \) due capacitive current path between beam and electrodes and motional current \( i_{mot} \) due to motion of the beam end.
1. Capacitive Excitation

We use electrostatic force to actuate the beam. This requires a large dc voltage $v_{dc}$ over the narrow gap between the beam end and the electrode superposed by an ac voltage $v(t)$ at the actuation frequency. The energy stored in the capacitor $C$ formed by the electrode and the beam end.

$$E = \frac{1}{2} CV^2$$

$$E = \frac{1}{2} C (v^2_{dc} + 2v_{ac}v_{dc} + v^2_{ac})$$

Thus, we have force at three frequencies: a dc-force, force at excitation frequency $f$ due to cross term $2v_{ac}v_{dc}$, and force at twice the excitation frequency due to square term $v_{ac}^2$. The last term is very small so ignore it. The plate capacitance is

$$C = \frac{A_{el}}{d - x}$$

Where, $\varepsilon$ is the permittivity of free space, $A_{el}$ is the electrode area, $d$ is the initial electrode gap, and $x$ is again the movement of the beam tip. The force is obtained from

$$f = \frac{\partial E}{\partial x} = \varepsilon^2 x A_{el}$$

$$f = v_{ac}v_{dc}e^2 \frac{A_{el}}{d^2} = \eta v_{ac}$$

A

Where we have defined a new variable, the electro mechanical transduction factor,

$$\eta = \frac{A_{el}}{d^2}$$

2. Resonator Current

The current through the resonator is

$$i = \frac{\partial C}{\partial t} = \frac{\partial v}{\partial t} + v \frac{\partial C}{\partial t}$$

With the approximations we take the equation,

$$i = c_0 \frac{\partial v}{\partial t} + \eta \frac{\partial x}{\partial t}$$

3. Electrical Equivalent Circuit

We are now ready to develop an equivalent circuit for our beam resonator using above equations,

$$\frac{M}{n} \ddot{x}_m + \frac{L}{n} i_m + \frac{k}{n} \int i_m \, dt = f(t)$$

Next, remembering that $f = \square u_{ac}$
By defining motional resistance, motional capacitance, and motional inductance as,

\[
R_m = \frac{\sqrt{km}}{Q q^2}
\]

\[
C_m = \frac{n^2}{k}
\]

\[
L_m = \frac{M}{n^2}
\]

C. Amplifier Design

Amplification is an essential function in most analog circuit. The block diagram of two stage Op-Amp. It consists of two stages in which first is differential stage and second is the gain stage. Output buffer is also used. In CMOS Op-Amp compensation network is used to which a feedback network is applied around the amplifier in virtually all Op-Amp applications. Transistors M1, M2, M3, and M4 form the first stage of the op amp the differential amplifier with differential to single ended transformation. Transistors M1 and M2 are standard N channel MOSFET transistors which form the basic input stage of the amplifier. The gate of M1 is the inverting input and the gate of M2 is the non-inverting input. A differential input signal applied across the two input terminals will be amplified according to the gain of the differential stage. The second stage is a current sink load inverter.

The purpose of the second gain stage, as the name implies, is to provide additional gain in the amplifier. Consisting of transistors M5 and M6, this stage takes the output from the drain of M2 and amplifies it through M5 which is in the standard common source configuration. Again, similar to the differential gain stage, this stage employs an active device, M6, to serve as the load resistance for M5. The gain of this stage is the transconductance of M5 times the effective load resistance comprised of the output resistances of M5 and M6. M6 is the driver while M7 acts as the load.
Table 2: Requirement of Differential Amplifier

<table>
<thead>
<tr>
<th>Design Specification</th>
<th>Value</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>VDD</td>
<td>1.8</td>
<td>v</td>
</tr>
<tr>
<td>Gain</td>
<td>100</td>
<td>≥60db</td>
</tr>
<tr>
<td>Load Capacitance</td>
<td>2</td>
<td>Pf</td>
</tr>
<tr>
<td>Input Common Mode Range(+)</td>
<td>1.6</td>
<td>v</td>
</tr>
<tr>
<td>Input Common Mode Range(-)</td>
<td>0.8</td>
<td>v</td>
</tr>
<tr>
<td>Slew Rate</td>
<td>20</td>
<td>v/u sec</td>
</tr>
<tr>
<td>Power Dissipation</td>
<td>≤300</td>
<td>uw</td>
</tr>
<tr>
<td>Gain Bandwidth</td>
<td>30</td>
<td>MHz</td>
</tr>
<tr>
<td>Technology Used</td>
<td>180</td>
<td>nm</td>
</tr>
</tbody>
</table>

Design Procedure:

- Find $I_5$ from slew rate.
- $M_3$, $M_4$ get from ICMR(+)
- $M_1$, $M_2$ get from Gain bandwidth.
- $M_5$ get from ICMR(-)
- $M_8$ will get from slew rate and $M_5$.  

Slew rate

Before finding slew rate we have to know, the value of $C_c$.

- $C_c \geq 0.22CL$
- $C_c = 2PF$
- $C_c \geq 0.44fF$

$$\text{Slew Rate} = \frac{I_5}{C_c}$$

$I_5 = 16uA$

Fixing ...$I_5 = 20uA$.

Design of $M_1$ and $M_2$

$$\left( \frac{W}{L} \right)_{1,2} = \frac{g_m}{u_m C_{ox}} \cdot 2L_d$$

$$\left( \frac{W}{L} \right)_{1,2} = \frac{g_m}{u_m C_{ox}} \cdot I_5$$

$$\left( \frac{W}{L} \right)_{1,2} = \frac{(160)^2}{300} \cdot 20$$

$$\left( \frac{W}{L} \right)_{1,2} = 5$$

Similarly we calculated width and length ratio for $m_3$, $m_4$, $m_5$, $m_6$, $m_7$, $m_8$.

D. Oscillator Design

An Oscillator is basically an amplifier with “Positive Feedback”, or regenerative feedback (in-phase) and one of the many problems in electronic circuit design is stopping amplifiers from oscillating while trying to get oscillators to oscillate. Oscillators work because they overcome the losses of their feedback resonator circuit either in the form of a capacitor, inductor or both in the same circuit by applying DC energy at the required frequency into this resonator circuit. In other words, an oscillator is an amplifier which uses positive feedback that generates an output frequency without the use of an input signal. It is self-sustaining. Then an oscillator has a small signal feedback amplifier with an open-loop gain equal to or slightly greater than one for oscillators to start but to continue oscillations the average loop gain must return to unity. In addition to these reactive components, an amplifying device such as an Operational Amplifier or Bipolar Transistor is required. Unlike an amplifier there is no external AC input required to cause the Oscillator to work as the DC supply energy is converted by the oscillator into AC energy at the required frequency.

$$A_V(V_{in} - BV_{out}) = V_{out}$$

$$A_VV_{in} - A_VBV_{out} = V_{out}$$
Oscillators are circuits that generate a continuous voltage output waveform at a required frequency with the values of the inductors, capacitors or resistors forming a frequency selective RLC resonant tank circuit and feedback network. This feedback network is an attenuation network which has a gain of less than one (β<1) and starts oscillations when \( A\beta>1 \) which returns to unity (\( A\beta=1 \)) once oscillations commence.

### III. Simulation Result

The two- stage CMOS Operational amplifier and MEMS Oscillator is done using Cadence software. Design parameters of two stage CMOS Op-amp and MEMS Resonator are summarized in table 3.

#### Table 3: Design Parameters

<table>
<thead>
<tr>
<th>TWO Stage Op-amp</th>
<th>Width</th>
<th>Length</th>
</tr>
</thead>
<tbody>
<tr>
<td>M1, M2</td>
<td>3</td>
<td>500 nm</td>
</tr>
<tr>
<td>M3, M4</td>
<td>7</td>
<td>500 nm</td>
</tr>
<tr>
<td>M5</td>
<td>6</td>
<td>500 nm</td>
</tr>
<tr>
<td>M6</td>
<td>87</td>
<td>500 nm</td>
</tr>
<tr>
<td>M7</td>
<td>37.5</td>
<td>500 nm</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>MEMS Resonator</th>
<th>Value</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Resistance, ( R_M )</td>
<td>2.8</td>
<td>MΩ</td>
</tr>
<tr>
<td>Inductance, ( L_M )</td>
<td>13.6</td>
<td>H</td>
</tr>
<tr>
<td>Capacitance, ( C_M )</td>
<td>2.46</td>
<td>aF</td>
</tr>
<tr>
<td>Feed through capacitance</td>
<td>2</td>
<td>fF</td>
</tr>
</tbody>
</table>

Fig. 5 shows the designed circuit of two stage operational amplifier. The circuit is designed with Vss of -1.8v and Vdd of 1.8v. Fig. 7 shows the proper simulated design of MEMS oscillator circuit with two stage operational amplifier. The circuit is designed with motional resistance of \( R_m=2.8\)MΩ, capacitance, \( C_m=2.46\)aF and inductance, \( L_m=13.6\)H respectively. Two stage operational amplifier output is directly connected to input of equivalent circuit model of MEMS beam resonator.
Table 3 shows the design parameter of both two stage operational amplifier and MEMS Beam Resonator.

Fig. 8(A): AC Response of MEMS based Oscillator

Fig. 8(B): AC Response of MEMS based Oscillator

Fig. 9: Transient Response of MEMS Oscillator

Fig. 10: Frequency Response of MEMS Oscillator
Fig 8(a and b) provides AC response of the circuit which obtained by applying ac signal to the input of amplifier and measuring from output of MEMS Oscillator. A gain of 18.17 dB is obtained at 19.8 MHz resonance frequency and also phase of the oscillator is 115 degree thus it satisfies the condition. Fig 9 provides transient response of MEMS oscillator providing the period of output signal is 0.1 us. As depicted at Fig 10 shows the oscillation frequency is 19.8 MHz obtained from frequency response of the MEMS beam resonator.

IV. Conclusion

The MEMS beam resonator and two stage Operational amplifiers were designed and simulated. This methodology was able to achieve the 19.8 MHz oscillation at gain 18.17 dB using 1.8 V, VDD and phase of 115 degree at 19.8 MHz compared to 13 dB in [1]. Further Improvement shall be done for this CMOS-MEMS oscillator in term of its oscillation frequency and phase noise performance.

Acknowledgement

The authors would like to acknowledge the support provided by the centre in VLSI Lab, SRM University.

References