

Low Power Devices for an Electronic Adapter with Mitigating the Soft Errors

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Abstract: *This work presents the implementation of a high efficiency adapter using voltage regulators along with the converters. A new structure of the slim-type processor is proposed, which is composed of copper wire as the primary winding and printed circuit-board winding on the outer layer as the secondary winding be layout drawn using a Cadence virtuoso tool and by the simulation process it is tested in a 180nm CMOS technology. The proposed structure is suitable for a slim and high-efficiency regulator as well as converter because it has advantages of easy utilization and wide conductive cross sectional area compared to the other works. In addition the voltage doublers rectifier is applied to the secondary side due to its simple structure of secondary winding, and a CLC filter is adopted to reduce the output filters size. The validity of this study is confirmed by the experimental results.*

Index Terms: *CLC output filter, slim-type transformer, and voltage-doubler rectifier.*

I. Introduction

Household appliances must have a network processor in order to communicate with other appliances through a home network. This additional processor increases the cost of the appliance when it is embedded. In addition, the communication medium for a home network has not converged to a unique standard at present [1], [2]. Therefore, there is a need for adapters that convert appliance data /messages to the correct home network format. Recently, the communication protocol between an appliance and an adapter has been standardized [2], the CACI (Compact Appliance Control Interface) was reported in [1]. But now, it is necessary to develop adapters, because many appliances still use proprietary communication protocols. This paper describes the adapter, which can download communication protocol conversion programs, and can commonly be used for various appliances. In addition, household appliances must be able to increase power supply capacity in order to supply electrical power to the adapter. In this paper, we selected IEEE 802.11b wireless LAN as a communication medium for the home network; however, other types of networks are easily supported. Household appliances must be capable of supplying peak power that enables transmitting or receiving 802.11b frames. This peak power is about 1W, and the power save mode (transceiver is periodically in active) is the same as the normal mode (transceiver is always in active) [3], [4]. This paper describes how to reduce the peak power using the smoothing circuit (SC) and the dynamic bandwidth controller (DBC). An outline of this paper is as follows, in section II we briefly describe the home network architecture in which our adapter operates, section III introduces the software architecture of the adapter itself and details its functionalities, section IV discusses power management issues when connecting the adapter to existing appliances. Section V offers some concluding remarks.

Review Of Methods

A. RT Method

In the RT method [2], [3], one port of the adapter is connected to a calibrated port of the VNA, and the other port is terminated in a reflective load, as shown in Fig. 1. The reflection coefficient of the adapter and load is measured at reference plane 2, and the process is repeated with the reflective load replaced by a second reflective load, whose reflection coefficient differs in phase from that of the first. In practice, the reflective terminations are typically an offset short and an offset open for a coaxial port and a flush short and an offset short for a waveguide port. The relation between the reflection coefficients measured at reference plane 2 in Fig. 1 and the intrinsic efficiency of the adapter was derived in [3]. Assuming a reciprocal, low-loss adapter.

Proposed Structure

A. Slim-Type Transformer

The use of PCB trace as both the primary and the secondary winding has been researched for the reduced height of the transformer as shown in Fig. 2. Although this method can make it easy to build the slim transformer, large portion of the PCB cannot be used for conduction since many turns need wide space for the

insulation. Therefore, conduction loss can be increased due to the reduced conductive cross-sectional area. Also, the high cost for the manufacturing of each PCB winding can be the problem. To overcome these drawbacks, the sandwiched or interleaving structure using copper wire as the primary winding and PCB trace as the secondary winding was researched as shown in Fig. 2. The copper wire is more suitable than the PCB trace for the plenty of turns in the primary side. However, it cannot use the enough window area of the slim transformer, considering the insulation between the primary and the secondary winding. It is also difficult to utilize the spiral-wound coil of the primary winding without a bobbin.

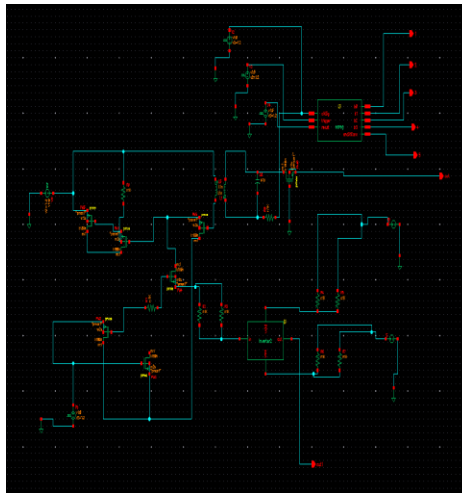


Fig.1. structure of the proposed scheme

The proposed structure of the slim-type transformer is composed of copper wire on the bobbin as the primary winding and PCB winding on the outer layer as the secondary winding. In the slim-type transformer, the insulation between the primary and the secondary winding takes considerable portion of the window area when the windings are stacked as shown in Fig. 2 and 3. However, when the primary and secondary windings are positioned inside and outside, respectively, the volume of insulation between them can be greatly decreased as shown in Fig. 2(c). The saved area can be contributed to the conductive cross-sectional area, and the conduction loss can be reduced. Also, the utilization of the primary winding is easier due to the use of the bobbin. Additionally, the leakage inductance of the transformer is a very important parameter in the LLC resonant converter since it is directly related to the resonance and this value can be controlled by using this structure. The leakage inductance is determined by the distance between the primary and secondary windings generally, and the distance can be designed from the PCB pattern. Therefore, the LLC resonant converter can be implemented without the need to add an external resonant inductor.

Voltage-Doubler Rectifier and CLC Output Filter

The topology selection of the secondary side is also very important to build a high-efficiency and slim-type converter since it determines the design of the transformer. As shown in Fig. 3, the center-tapped, full-bridge, and voltage-doubler rectifiers can be attractive candidates of the secondary-side rectifier. Among many characteristics, the configuration of these secondary winding is very critical to the design of the slim-type transformer since large current is flowing through these secondary side and wide conductive area is needed. Assume that the turns ratios of the transformer in the center-tapped, full bridge, and voltage-doubler rectifiers are $n_{p_ct}:n_{s_ct}$, $n_{p_fb}:n_{s_fb}$, and $n_{p_vd}:n_{s_vd}$, respectively. When the primary turns of aforementioned rectifiers are the same, n_{s_ct} and n_{s_fb} are designed twice n_{s_vd} to obtain the same voltage conversion ratio.

Additionally, since the center-tapped rectifier configuration needs two different secondary windings, it is not appropriate to the slim-type transformer due to its complex structure of the secondary PCB winding. In this point of view, the voltage-doubler rectifier can be selected for an optimal one with the proposed transformer, which can be realized with just one turn in the secondary winding. Meanwhile, when the capacitor output filter is adopted, the current flowing through the output capacitor is discontinuous. Therefore, the rms ripple current of the output capacitor is generated very large, and the bulky capacitors are needed to bear that. To reduce the output filter size in that case, a CLC filter is widely used as shown in Fig. 4(a). Due to the additional small inductor, large current ripple is generated in the left-side capacitor C_f , and the current ripple of the right-side capacitor C_O is very small. Thus, the multilayer ceramic capacitor is applied to C_f since it has high rms ripple current rating and small size, and the electrolytic capacitor is applied to C_O since it has large capacitance value for dynamic

response. With the voltage-doubler rectifier, using the CLC filter is more attractive since the additional passive components are less than with other rectifiers. Because doubler capacitors, C_1 and C_2 , are already in the voltage-doubler rectifier, only the additional small inductor is needed to achieve the CLC filter configuration as shown in Fig. 4.

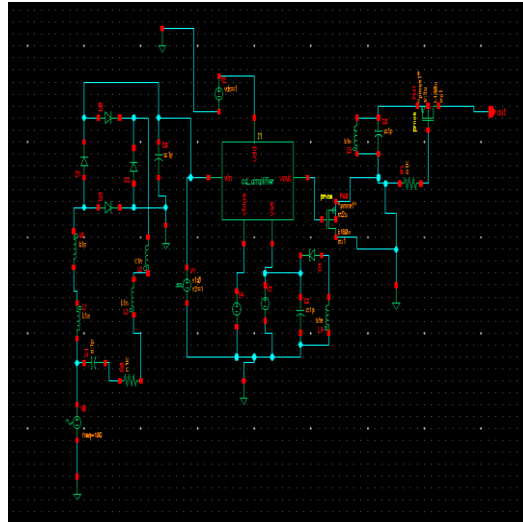


Fig.2.implementing the proposed adapter

II. The Legacy System Challenge

Legacy systems present a number of technical challenges to achieving net-centric standards compliance. These issues often include: software architectures [16], data formats, external interfaces, and constraints of safety and security. These challenges are discussed below.

A. Legacy Software Architectures

The typical legacy tactical system in DoD today was not designed to comply with net-centric technical standards in either their internal software architectures or their external interfaces. The authors have observed that many of these systems were not designed to use some of the key Internet-related standards, such as HTML, HTTP, SOAP, WSDL, and XML. The reasons for this include: 1) the inability of Internet standards to satisfy real-time, mission critical requirements [12]–[18] and 2) the computing standards and commercial products available at the time many of today’s military systems were developed [12]–[17].

The authors have observed two prevalent mechanisms used for communications within legacy military software architectures by server and client software applications: a) Remote Procedure Calls (RPC) made using Application Programming Interfaces (APIs), and b) the Common Object Request Broker Architecture (CORBA) [12]. Both RPC/APIs and CORBA use the Transmission Control Protocol/Internet Protocol (TCP/IP) for message transmission. Fig. 1 depicts the use of RPC over socket-based APIs to support communications between client and server software applications over a network. The figure shows a client application and a server application connected to a common network. The client needs to perform a function on some data, while the server can perform that function. To initiate this function call, the client uses an RPC to remotely call the server function. The client uses software “sockets” and TCP/IP to call the function. On the network, TCP/IP is used to transmit the function call and provide it on the proper socket.

The server recognizes the RPC, performs the function, and provides the results back to the client. Fig. 2 depicts the use of CORBA to support communications between client and server software applications over a network. CORBA, a specification developed by the Object Management Group [19], is an object-oriented counterpart to a socket-based API. In the object-oriented paradigm, the calling application invokes a “method” on an object. An Object Request Broker (ORB), which is a software implementation of the CORBA. Specification keeps track of the locations of software and objects on the network. It determines the location of Object A’s implementation and invokes the appropriate method on that object.

B. Legacy Data Formats and Access

Data in a typical legacy system are held in legacy formats. Often these consist of custom and proprietary data formats and conventions that were selected by the developer to initially implement each legacy system. Legacy formats may be due to the lack of data standards and/or the lack of interoperability requirements at the time the system was developed. In addition, the legacy data is rarely accessible via standard, net-

centric mechanisms (e.g., web services). Accessing and translating legacy data often requires a very large engineering effort.

C. Legacy Point-to-Point Interfaces

Traditionally in DoD, data exchange between two systems has been defined for point-to-point interfaces [7]. These interfaces are numerous and essentially proprietary, because they only work for a pair of systems. On the other hand, net-centric compliance requires a “many-to-many” data exchange approach [7]. This requires a more open approach to defining interfaces, as well as the implementation of net-centric technical standards such as XML.

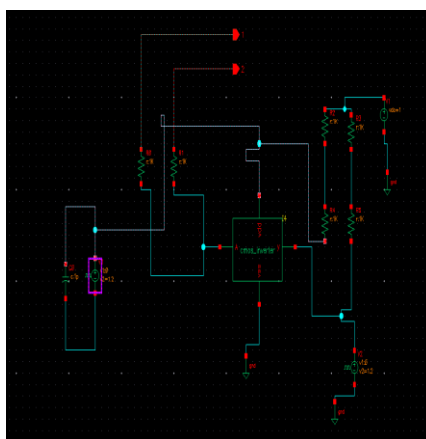


Fig.3. circuit for the proposed scheme operation cycle

D. Safety and Security Constraints

Legacy military systems typically have important safety and security constraints on their design and performance. For example, weapon systems are concerned with weapon safety requirements, such as maintaining positive control of their weapons under all circumstances. They may also be concerned with issues such as crossing security domains when connecting to the GIG or with other systems with which interfaces are required. As an example, the authors have observed that this is typical for systems on a ship, where a mixture of multiple security domains exists, due to a variety of classification levels, access authorization, and “need to know.” As a result, radically changing the software architectures of such systems greatly increases risk and invites mission critical failure. Despite great advances in software technologies, current enterprise standards, tools and implementations are largely unable to meet the most challenging real-time, deterministic requirements of these critical systems [20]–[21].

III. Net-Centric Adapter

A. Net-Centric Adapter Concept

The goal of the Net-Centric Adapter for Legacy Systems (NCALS) is to enable legacy military systems to affordably participate in a net-centric force. Specifically, NCALS minimizes the changes to legacy systems required to participate on the GIG. In addition, it is designed as a common, generic software component that could be used by many different legacy systems. To achieve this, several technical objectives were necessary:

- Enable legacy systems to publish their data and services to the GIG.
- Enable legacy systems to subscribe to GIG data and services.
- Reduce the software development effort required to implement net-enabled legacy systems.
- Architect the NCALS software as a common, configurable component.

The NCALS software concept is illustrated in Fig. 3. It serves as an automated, two-way gateway between a legacy system and the GIG. As such, it works behind the scenes in an automated fashion to expose data from legacy systems to users of the GIG. In addition, it must be configurable, as a common software component, to support a variety of legacy systems needs, as well as portable across a variety of computing platforms.

Lastly, its architecture must be scalable to accommodate the net-centric data requirements of many different legacy systems. A legacy system consists of legacy software components running on hardware, typically on a Local Area Network (LAN). These software components communicate with one another via legacy software interfaces (e.g., APIs and/or CORBA) and hold data in legacy formats. The NCALS software uses the existing legacy software interfaces to obtain legacy data, transform it into net-centric standard formats and publish it to the GIG in compliance with net-centric standards. Likewise, it transforms GIG data into legacy data formats and injects it into the legacy system via its existing software interfaces. Regardless of the particular domain, data and services of a legacy system, much of the NCALS functionality is common. However, it is

configurable to accommodate the specifics of a particular legacy system operating in its specific domain of operations. Since NCALS is designed to enable net-centric operations, it focuses on enabling legacy systems to interoperate with the GIG. As a result, it provides a service-oriented architecture connection for a legacy system to the rest of the DoD enterprise on the GIG. However, it does not modify the legacy components to comply internally with the net-centric technical standards. It allows the legacy system architecture to remain largely undisturbed.

IV. Results

The simulation results for the dialysis session without the adapter demonstrate that the basic flow features are a high-speed jet (average u-component of 270 cm/s at the x-location of the needle tip) issuing from the needle and a slower background vascular access flow (average u-component of 60 cm/s at the x-location of the needle tip) within the graft [Fig. 1]. Downstream of the needle tip, the needle flow impinges upon the bottom of the graft wall and begins to sweep upwards along the sides to the top of the graft, forming two large-scale helical structures that persist down the length of the graft. Immersed within these helical structures are a host of small-scale, complex, three-dimensional flow structures that result in substantial flow unsteadiness downstream of the dialysis needle. The graft wall experiences increased hemodynamic stresses as a result of the impinging needle jet.

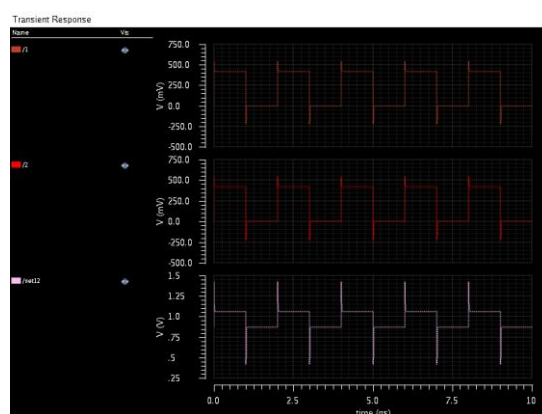


Fig.4. Transient analysis for the proposed scheme

This is evidenced by a sharp rise in the WSS [Fig. 2] field in the vicinity of the jet impingement region. Upstream of this region, the WSS is approximately 30 dynes/cm², but downstream of it, the WSS rapidly undergoes an eightyfold increase to nearly 2400 dynes/cm². When the fully deployed SMP adapter is present, the needle jet is oriented in the downstream direction and no longer impinges as severely upon the graft wall [Fig. 1]. As a result, the WSS [Fig. 2] is substantially reduced. In addition, the larger cross-sectional exit area of the adapter decelerates the needle flows. A comparison of the average u-component of velocity (130 cm/s) of the needle flow at the distal tip of the adapter with the average u-component of velocity (70 cm/s) of the background vascular access flow at the same x-location shows them to be closer in value, which could potentially reduce the strength of the shear layer instability that develops downstream of the adapter. Images of the *in vitro* flow visualization prior to delivery of the SMP adapter are shown in Fig. 2 and 3.

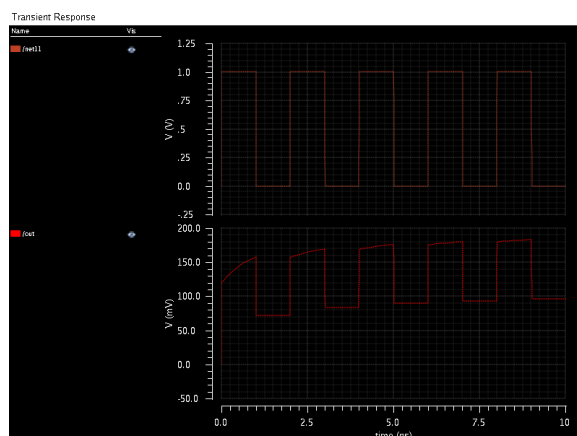


Fig.5. Transient analysis for the architecture

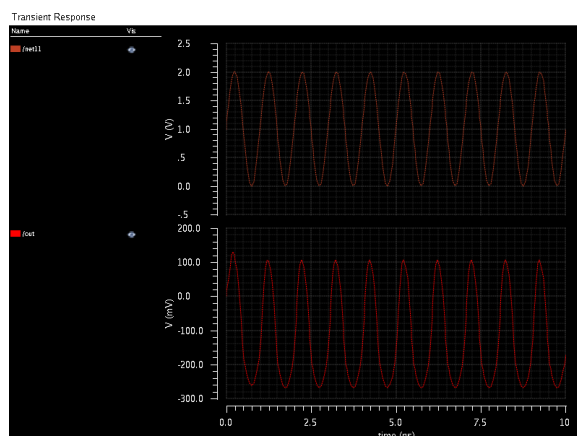


Fig.6. Transient analysis for the clocking cycle

Although the vascular access and needle flow rates and Reynolds numbers are well below the physiological values used in the CFD simulations, a qualitatively similar flow field is present in which the needle jet impinges upon the wall and wraps upward along the sides of the wall. After the compressed SMP adapter is delivered through the dialysis needle, the heated flow within the model graft actuates the adapter to its fully expanded shape [Fig. 5]. Complete actuation occurs within approximately 30 s. Following the actuation, room temperature water (21 °C) is pumped through the graft model. The resulting flow visualization image [Fig. 3] with the deployed adapter demonstrates that the adapter directs the needle jet so that it is somewhat more aligned with the background vascular access flow.

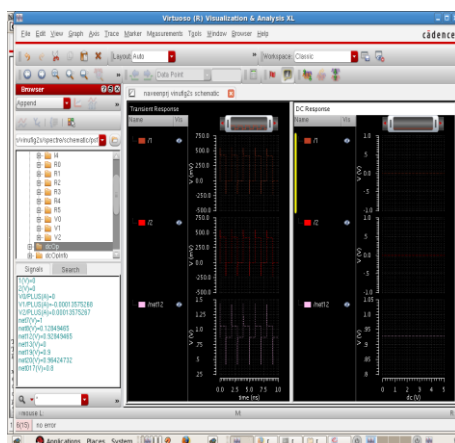


Fig.7. Transient analysis and DC analysis

A. Monitoring of Load-Connection Block

The MOLB monitors whether the load system is connected or not. For this function, the MOLB generates vCI-ON and sends it over to the CIOB when the load system is connected. The MOLB can be realized by using only one comparator and several resistors as shown in Fig. 2. When the load system is connected to the proposed adapter, since vDET and vCI-ON are “ON,” optocouplers opto1 and opto2 are turned ON. Then, the CIOB connects vlink to the HV pin of the control IC and vCC to the VCC pin of the control IC. Consequently, the control IC starts its function. Each resistance can be designed to satisfy (1). R3 and R4 can be designed considering the operating current of opto1 and opto2.

Table I:

Part	Name
Diode	GBL407
Resistor	1M OHMS
Capacitor	250 nF
Link capacitor	200 uF
Supply voltage	3V
Output voltage	1.8V
Power	0.8mW
Current	1.2 A

Table II:

	This paper	[1]	[2]
Area mm ²	20	26	23
Power (mW) {x 10}	10	16	23
Current (mA)	8	10	12
Voltage (V)	3	5	4

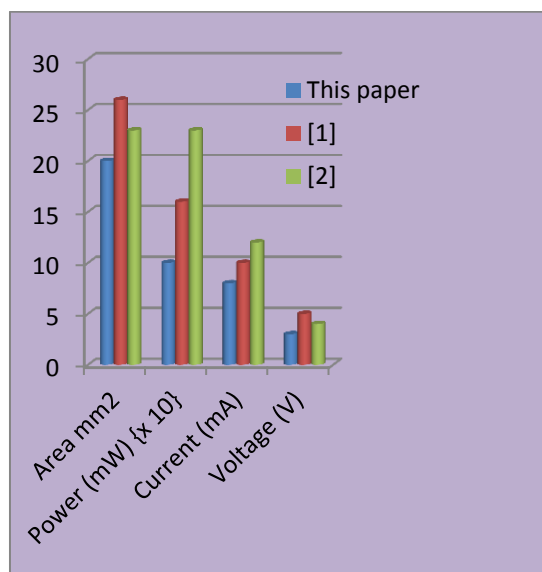


Fig.8. Comparative chart

V. Discussion And Conclusion

Through this initial investigation we have presented the results of a novel architecture for the newly designed for the low voltage level operations, dialysis needle adapter. The simulations demonstrate that the adapter significantly reduces the power and the area, which could potentially reduce the general subsequent and subsequent vascular access occlusions. The successful preliminary *in vitro* test indicates that the concept of delivering and actuating a SMP adapter through a dialysis needle has merit and should be further investigated and improved. It is evident that the prototype adapter does not entirely direct the flow in the downstream direction. This can be remedied by casting a longer regulators and adapter about a mandrel that has an inherently curved shape. Given the ease of machining and molding the material this and other modifications can readily be incorporated into alternate adapter design. This can be implemented in the Cadence Virtuoso tool of 180nm CMOS process.

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