

## Mitigation of Harmonics for Grid-inter Connected DG system With Closed-Loop Power Control technique

Panchadarla lavanya<sup>1</sup>, K vijay kumar<sup>2</sup>

<sup>1</sup>(PG student of electrical and electronic dept,DIET, anakapalle)

<sup>2</sup>(Assoc.prof & H.O.D,EEE Dept,DIET,anakapalle)

---

**Abstract:** The proposed system presents power-control strategies of a grid-connected hybrid generation system with versatile power transfer. This hybrid system allows maximum utilization of freely available renewable energy sources like wind and photovoltaic energies. For this, an adaptive MPPT algorithm along with standard perturb and observes method will be used for the system. Also, this configuration allows the two sources to supply the load separately or simultaneously depending on the availability of the energy sources. The turbine rotor speed is the main determinant of mechanical output from DG energy and Solar cell operating voltage in the case of output power from solar energy.

The inverter converts the DC output from non-conventional energy into useful AC power for the connected load. In order to utilization distributed generation (DG) unit interfacing converters to actively compensate harmonics, this paper proposes an enhanced current control approach, which seamlessly integrates system harmonic mitigation capabilities with the primary DG power generation function. As the proposed current controller has two well- decoupled control branches to independently control fundamental and harmonic DG currents, local nonlinear load harmonic current detection and distribution system harmonic voltage detection are not necessary for the proposed harmonic compensation method. Moreover, a closed-loop power control scheme is employed to directly derive the fundamental current reference without using any phase-locked loops (PLL). The proposed power control scheme effectively eliminates the impacts of steady-state fundamental current tracking errors in the DG units. Thus, an accurate power control is realized even when the harmonic compensation functions are activated hybrid system operates under normal conditions which include normal room temperature in the case of solar. The simulation results are presented to illustrate the operating principle, feasibility and reliability of this proposed system.

**Keywords:** Index Terms-Active power filter, distributed generation, harmonic compensation, harmonic extraction, phase-locked loop (PLL), resonant controller, virtual impedance.

---

### I. Introduction

DUE to the growing importance of renewable-energy based power generation, a large number of power electronics interfaced DG units have been installed in the low-voltage power distribution systems [1]. It has been reported that the control of interfacing converters can introduce system resonance issues [2]. Moreover, the increasing presence of nonlinear loads, such as variable-speed drives, light-emitting diode (LED) lamps, compact fluorescent lamps (CFLS), etc., will further degrade distribution system power quality.

To compensate distribution system harmonic distortions, a number of active and passive filtering methods have been developed [3]. However, installing additional filters is not very favorable due to cost concerns. Alternatively, distribution system power quality enhancement using flexible control of grid connected DG units is becoming an interesting topic [5]–[12], where the ancillary harmonic compensation capability is integrated with the DG primary power generation function through modifying control references. This idea is especially attractive considering that the available power from backstage renewable energy resources is often lower than the power rating of DG interfacing converters. For the local load harmonic current compensation methods as discussed in [5]–[12], an accurate detection of local load harmonic current is important. Various types of harmonic detection methods [4] have been presented, such as the Fourier-transformation-based detection method in [13], the detection scheme using instantaneous real and reactive power theory in [14], second-order generalized integrator (SOGI) in [15], and the delayed-signal-cancellation-based detection in [32]. Nevertheless, harmonic extraction process substantially increases the computing load of DG unit controllers. For a cost-effective DG unit with limited computing ability, complex harmonic extraction methods might not be acceptable. Alternatively, an interesting harmonic detection less method was proposed in [16] and [17]. It shows that the main grid current can be directly controlled to be sinusoidal, instead of regulating DG output current to absorb local load harmonics.

In this scenario, local load current is essentially treated as a disturbance in the grid current regulation loop. It should be noted that DG system normally has smaller stability margin when the direct control of grid current is employed. In addition, for the shunt active harmonic filtering via point of connection (PoC) harmonic

voltage detection (also named as resistive active power filter (R-APF) in [12] and [23]), the control techniques in [16] and [17] cannot be used. Alternatively, the recently proposed hybrid voltage and current control method (HCM) [30] also allows the compensation of local load harmonics without using any harmonic detection process, where the well-understand droop control scheme [22] is adopted to regulate the output power of the DG unit. Further considering that droop-control-based DG unit often features slow power control dynamics [21] and current-controlled DG units are more widely installed in the distribution system, developing a robust Current-control-based harmonic compensation method without using any system harmonic detection is very necessary. It is worth mentioning that the DG real and reactive power control performance shall not be affected during the harmonic compensation. To satisfy this requirement, the fundamental DG current reference shall be calculated according to power references. Conventionally, the fundamental current reference can be determined based on the assumption of ripple-free grid voltage with fixed magnitude, and the PLL is used to synchronize the fundamental current reference with the main grid. However, considering that PoC voltage magnitude often varies due to the distribution system power flow fluctuations, this method may cause nontrivial power control errors. Alternatively, the fundamental current reference can also be calculated through the “power-current transformation” in [7], where only the detected PoC voltage fundamental component is used in the calculation. However, for a DG unit with the ancillary harmonic compensation capability, the interactions between distorted DG current and PoC harmonic voltages may contribute some DC real and reactive power bias [31], and these power bias cannot be directly addressed in the control method in [7].

In order to ensure accurate power tracking performance, a closed-loop DG power control is necessary. To simplify the operation of DG units with ancillary harmonic compensation capabilities while maintaining accurate power control, this paper proposes an improved current controller with two parallel control branches. The first control branch is responsible for DG unit fundamental current control, and the second one is employed to compensate local load harmonic current or feeder resonance voltage. In contrast to the conventional control methods with harmonic detection, the PoC voltage and local load current can be directly used as the input of the proposed current controller, without affecting the harmonic compensation accuracy of the DG unit. Moreover, with simple PI regulation in the outer power control loop, the proposed DG unit also achieves zero steady-state power tracking errors even when the fundamental current tracking has some steady-state errors. Simulated and experimental results from a single-phase DG unit

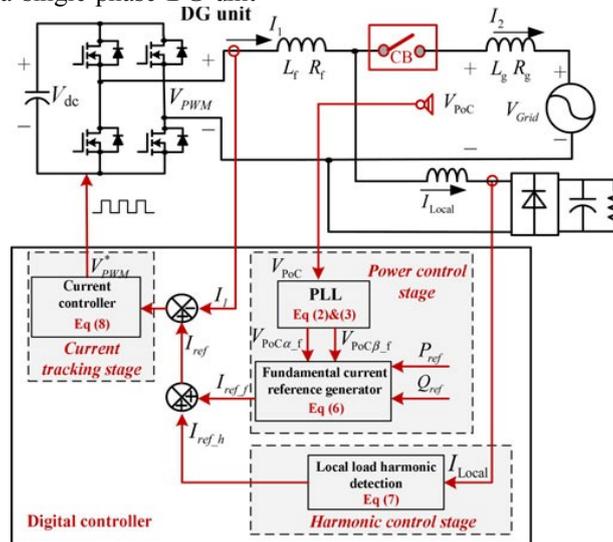


Fig. 1. DG unit with local load harmonic current compensation capability. synchronized with PoC voltage

$$(V_{PoC}) \text{ as } I_{ref\_f} = \frac{\cos(\theta) P_{ref} + \sin(\theta) Q_{ref}}{E^*} \quad (1)$$

Where  $\theta$  is the PoC voltage phase angle detected by PLL,  $P_{ref}$  and  $Q_{ref}$  are the real and reactive power references, and  $E^*$  is the nominal voltage magnitude of the system. However, the current reference generator in (1) is not accurate in controlling DG power, due to variations of the PoC voltage magnitude. To overcome this drawback, an improved power control method with consideration of PoC voltage magnitude fluctuations [11] was developed as shown in Section II-B. First, the fundamental PoC voltage  $V_{PoC\alpha f}$  and its orthogonal component  $V_{PoC\beta f}$  (quarter cycle delayed respect to  $V_{PoC\alpha f}$ ) are obtained by using SOGI[15] as Validate the effectiveness of the proposed DG control method.

## II. Modeling Of Dg Unit With The Proposed Current Control Scheme

In this section, the harmonic compensation performance using the proposed current controller is investigated.

### A. Modeling of the Proposed Current Control Method

It is well understood that the current-controlled inverter shall be described as a closed-loop Norton equivalent circuit [27], [29]

$$I_1 = H_C(s)I_{ref} - Y_C(s)V_{PoC} \quad (4)$$

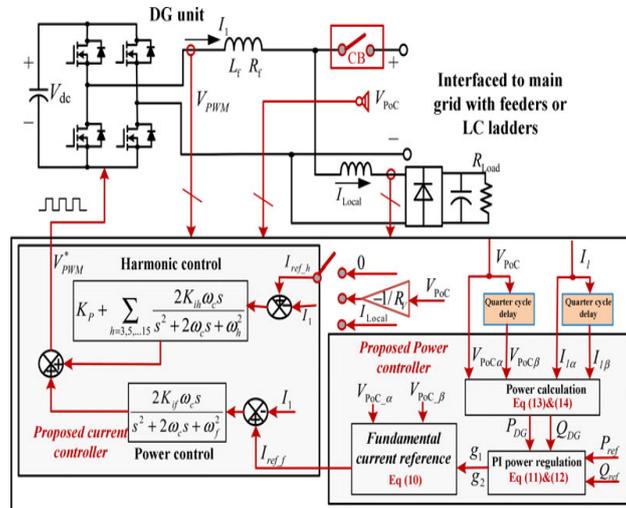


Fig.2 DG unit with the proposed control scheme

### B. Grid Operation

The DG Grid can operate in two modes. In grid-tied mode, the main converter is to provide stable dc bus voltage and required reactive power and to exchange power between the AC and DC buses. The boost converters are controlled to provide the maximum power. When the output power of the DC sources is greater than the dc loads, the converter acts as an inverter and injects power from DC to AC side. When the total power generation is less than the total load at the dc side, the converter injects power from the AC to DC side. When the total power generation is greater than the total load in the DG Grid, it will inject power to the utility grid. Otherwise, the DG Grid will receive power from the utility grid. In the grid tied mode, the battery converter is not very important in system operation because power is balanced by the utility grid. In autonomous mode, the battery plays a very important role for both power balance and voltage stability. Control objectives for various converters are dispatched by energy management system. DC bus voltage is maintained stable by a battery converter or boost converter according to different operating conditions. The main converter is controlled to provide a stable and high quality ac bus voltage. Both PV can operate on maximum power point tracking (MPPT) or off-MPPT mode based on system operating requirements. Variable wind speed and solar irradiation are applied to the PV arrays respectively to simulate variation of power of ac and dc sources and test the MPPT control algorithm.

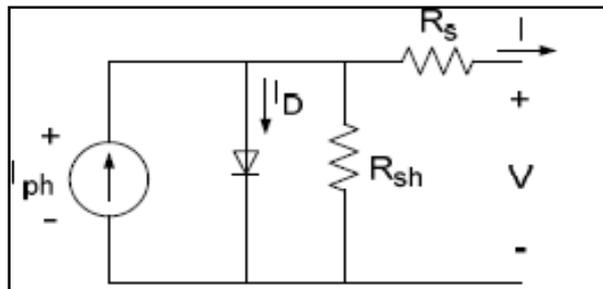


Fig. 3. Equivalent circuit of a solar cell.

### C. Modeling of PV Panel

Fig. 3 shows the equivalent circuit of a PV panel with a load. The current output of the PV panel is modeled by the following three equations [11], [12]. All the parameters are shown in Table I:

$$I_{pv} = n_p I_{ph} - n_p I_{sat} \times \left[ \exp \left( \left( \frac{q}{AkT} \right) \left( \frac{V_{pv}}{n_s} + I_{pv} R_s \right) \right) - 1 \right] \quad (5)$$

$$I_{ph} = (I_{sso} + k_i (T - T_r)) \times \frac{S}{1000} \quad (6)$$

$$I_{sat} = I_{rr} \left( \frac{T}{T_r} \right)^3 \exp \left( \left( \frac{qE_{gap}}{kA} \right) \left( \frac{1}{T_r} - \frac{1}{T} \right) \right) \quad (7)$$

### III. Coordination Current Control Of The Converters

There are five types of converters in the DG Grid. Those converters have to be coordinately controlled with the utility grid to supply an uninterrupted, high efficiency, and high quality power to variable DC and AC loads under variable solar irradiation and wind speed when the DG Grid operates in both isolated and grid tied modes. The control algorithms for those converters are presented in this section.

#### A. Grid-Connected Mode

When the DG Grid operates in this mode, the control objective of the boost converter is to track the MPPT of the PV array by regulating its terminal voltage. The back-to-back ac/dc/ac converter of the DFIG is controlled to regulate rotor side current to achieve MPPT and to synchronize with ac grid. The energy surplus of the DG Grid can be sent to the utility system. The role of the battery as the energy storage becomes less important because the power is balanced by the utility grid. In this case, the only function of the battery is to eliminate frequent power transfer between the dc and ac link. The dc/dc converter of the battery can be controlled as the energy buffer using the technique [15]. The main converter is designed to operate bi-directionally to incorporate complementary characteristic of wind and solar sources [16], [17]. The control objectives of the main converter are to maintain a stable dc-link voltage for variable dc load and to synchronize with the ac link and utility system. The combined time average equivalent circuit model of the booster and main converter is shown in Fig. 4 based on the basic principles and descriptions in [18] and [19] for booster and inverter respectively.

Power flow equations at the dc and ac links are as follows:

$$P_{pv} + P_{ac} = P_{dc}L + P_b \quad (8)$$

$$P_s = P_w - P_{ac}L - P_{ac} \quad (9)$$

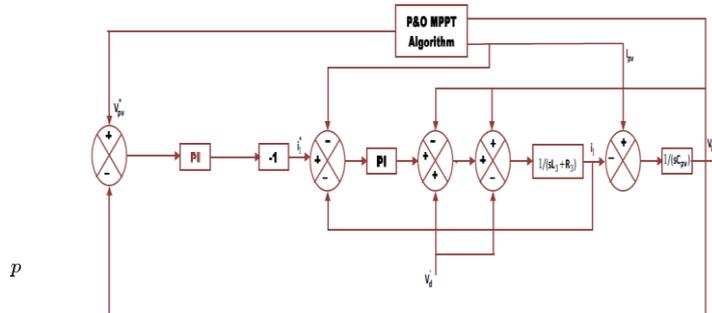


Fig.4. Time average model for the booster and main converter.

where real power  $P_v$  and  $P_w$  are produced by PV respectively,  $P_{ac}L$  and  $P_{dc}L$  are real power loads connected to ac and dc buses respectively,  $P_{ac}$  is the power exchange between ac and dc links,  $P_b$  is power injection to battery, and  $P_s$  is power injection from the DG Grid grid to the utility. The current and voltage equations at dc bus are as follows:

$$V_{pv} - V_T = L_i \frac{di_1}{dt} + R_i \quad (10)$$

$$I_{pv} - i_1 = C_{pv} \frac{dV}{dt} \quad (11)$$

$$V_T = V_d (1 - d_1) \quad (12)$$

$$i_1 (1 - d_1) - C_d \frac{dV_a}{dt} - \frac{1}{R_L} V_d - i_b - i_{ac} = 0 \quad (13)$$

Where  $d_1$  is the duty ratio of switch ST. Equations (10) and (11) show the ac side voltage equations of the main converter in ABC and  $d-q$  coordinates respectively [20]

$$L_2 \frac{d}{dt} \begin{bmatrix} i_A \\ i_B \\ i_C \end{bmatrix} + R_2 \begin{bmatrix} i_A \\ i_B \\ i_C \end{bmatrix} = \begin{bmatrix} V_{CA} \\ V_{CB} \\ V_{CC} \end{bmatrix} - \begin{bmatrix} V_{SA} \\ V_{SB} \\ V_{SC} \end{bmatrix} \quad (14)$$

$$L_2 \frac{d}{dt} \begin{bmatrix} i_d \\ i_q \end{bmatrix} = \begin{bmatrix} -R_2 & \omega L_2 \\ -\omega L_2 & -R_2 \end{bmatrix} \begin{bmatrix} i_d \\ i_q \end{bmatrix} + \begin{bmatrix} v_d \\ e \\ v_{cq} \end{bmatrix} - \begin{bmatrix} v_d \\ e \\ v_{sq} \end{bmatrix} \quad (15)$$

Where  $(v_{CA} v_{CB} v_{CC})$  ac side voltages of the main converter are,  $(v_{SA} v_{SB} v_{SC})$  are voltages across  $C_2$  (in Fig. 2, and  $)_q, (v_{sd} v_{sq})$  and  $(v_{cd} v_{cq})$  are the corresponding  $d-q$  co-

In order to maintain stable operation of the DG Grid under various supply and demand conditions, a coordination control algorithm for booster and main converter is proposed based on basic control algorithms of the grid interactive inverter in [19]. The control block diagram is shown in Fig. 5. The reference value of the solar panel terminal voltage  $V_*$  is determined by the basic perturbation and observation (P&O) algorithm based on solar irradiation and temperature to harness the maximum power [21], [22]. Dual-loop control for the dc/dc boost converter is described in [23], where the control objective is to provide a high quality dc voltage with good dynamic response. This control scheme is applied for the PV system to track optimal solar panel terminal voltage using the MPPT algorithm with minor modifications. The outer voltage loop can guarantee voltage reference tracking with zero steady-state error and the inner current loop can improve dynamic response.

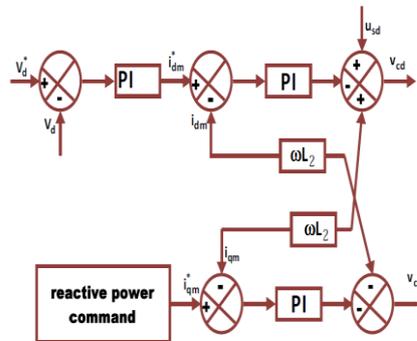


Fig. 5. The simulation control block diagram.

simulation Block and Results:

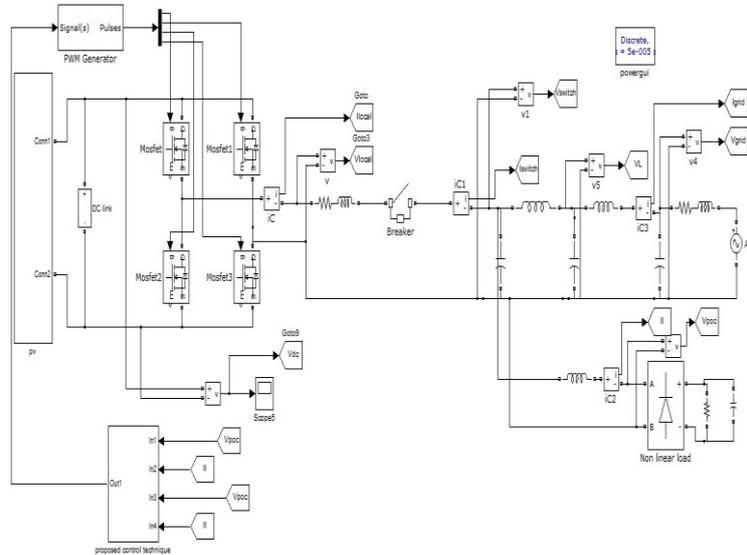
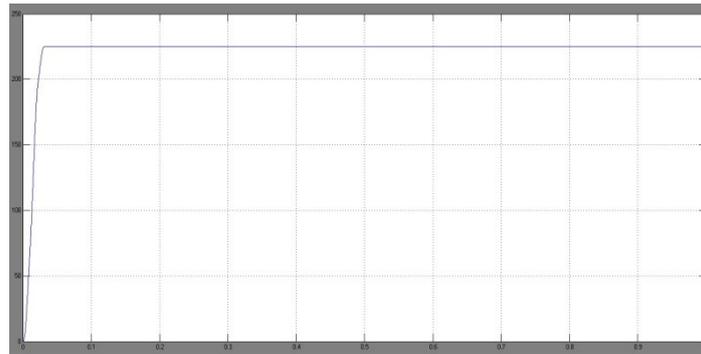
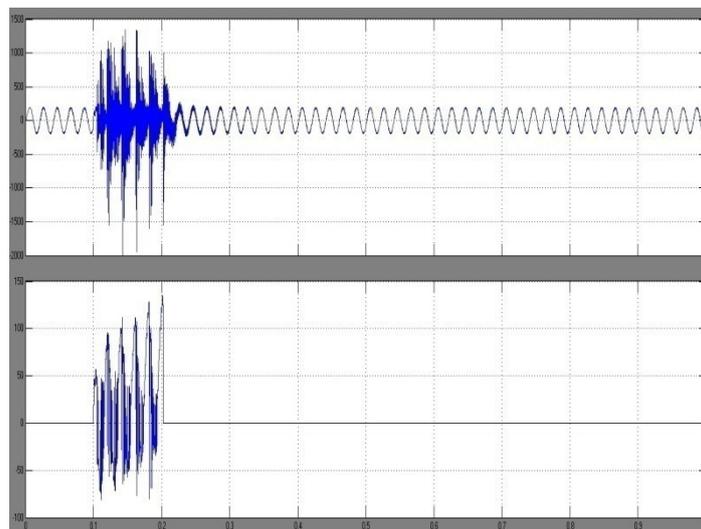


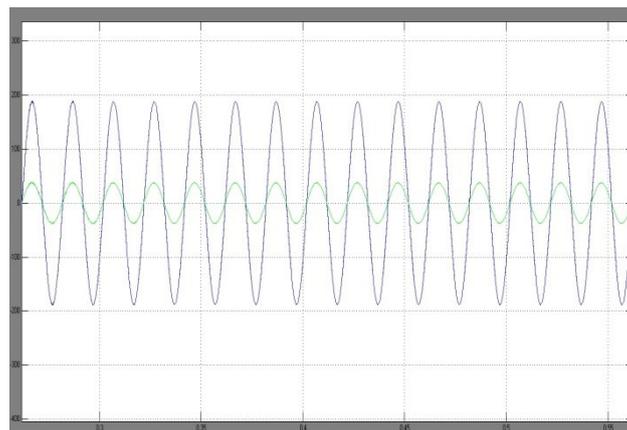
Fig.6 Proposed simulation Block with Grid and Load



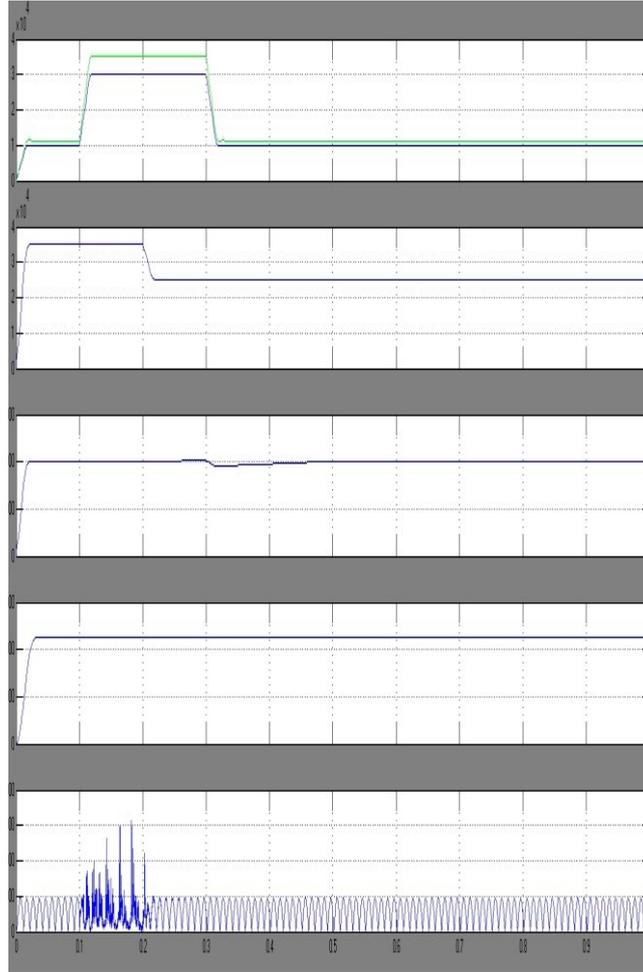
**Fig.7** Dc link Voltage



**Fig.8** Grid Load voltage and grid without controller



**Fig.9** Grid Load Voltage and Current in phase with each other



**Fig.10** Proposed PV cell voltage, PV power, without MPPT and With MPPT controller and distorted DC grid.

**Table I :** Parameters In Simulation Results

System Parameter	Value
Grid voltage	Simulation 230V/50Hz Experiment 115V/50Hz
DG filter	$L_f=6.5\text{mH}$ , $R_f=0.15\Omega$
Grid feeder	$L_g=3.4\text{mH}$ , $R_g=0.15\Omega$
LC ladder with five identical LC filter	$L=1.0\text{mH}$ , $C=25\mu\text{F}$ for each LC filter
Sampling/Switching frequency	20kHz/10kHz
DC link voltage	Simulation 550V Experiment 350V
<b>Power Control Parameter</b>	
Real power control $k_{p1}$ , $k_{i1}$	$k_{p1}=0.00001$ , $k_{i1}=0.001$
Reactive power control $k_{p2}$ , $k_{i2}$	$k_{p2}=0.00001$ , $k_{i2}=0.001$
LPF time constant $\tau$	0.0322 Sec
<b>Current control Parameter</b>	
Proportional gain $K_p$	48
Resonant gains $K_{ih}$	1500( $h=f$ ); 900 ( $h=3, 5, 7, 9$ ); 600 ( $h=11, 13, 15$ )
Resonant controller bandwidth $\omega_c$	4.1rad/s
$R_v$ (for PoC harmonic voltage compensation)	$5\Omega$

#### IV. Conclusion

In this paper, a simple harmonic compensation strategy is proposed for current-controlled DG unit interfacing converters. By separating the conventional proportional and multiple resonant controllers into two parallel control branches, the proposed method realizes power control and harmonic compensation without using

any local nonlinear load harmonic current extraction or PoC harmonic voltage detection. Moreover, the input of the fundamental power control branch is regulated by a closed-loop power control scheme, which avoids the adoption of PLLs. The proposed power Generation of PV Cell with MPPT control method ensures accurate power control even when harmonic compensation tasks are activated in the DG unit or the PoC voltage changes. Simulated and Simulation results from a single-phase DG unit verified the feasibility of the proposed strategy.

### Reference

- [1]. F. Blaabjerg, Z. Chen, And S. B. Kjaer, "Power Electronics As Efficient Interface In Dispersed Power Generation Systems," Ieee Trans. Power Electron, Vol. 19, No. 5, Pp. 1184–1194, Sep. 2004.
- [2]. F. Wang, J. L. Duarte, M. A. M. Hendrix, And P. F. Ribeiro, "Modeling And Analysis Of Grid Harmonic Distortion Impact Of Aggregated Dg Inverters," Ieee Trans. Power Electron, Vol. 26, No. 3, Pp. 786–797, Mar. 2011.
- [3]. L. Asiminoaei, F. Blaabjerg, S. Hansen, And P. Thogersen, "Adaptive Compensation Of Reactive Power With Shunt Active Power Filters," Ieee Trans. Ind. Appl., Vol. 44, No. 3, Pp. 867–877, May/Jun. 2008.
- [4]. L. Asiminoaei, F. Blaabjerg, And S. Hansen, "Detection Is Key—Harmonic Detection Methods For Active Power Filter Applications," Ieee. Ind. Appl. Mag., Vol. 13, No. 4, Pp. 22–33, Jul./Aug. 2007.
- [5]. N. Pogaku And T. C. Green, "Harmonic Mitigation Throughout A Distributed-Generator-Based Solution," In Ieee Proc. Gener. Transm. Distrib., Vol. 153, No. 3, Pp. 350–358, May 2006.
- [6]. C. J. Gajanayake, D. M. Vilathgamuwa, P. C. Loh, R. Teodorescu, And F. Blaabjerg, "Z-Source-Inverter-Based Flexible Distributed Generation System Solution For Grid Power Quality Improvement," Ieee Trans. Energy Convers., Vol. 24, No. 3, Pp. 695–704, Sep. 2009.
- [7]. R. I. Bojoi, G. Griva, V. Bostan, M. Guerriero, F. Farina, And F. Profumo, "Current Control Strategy Of Power Conditioners Using Sinusoidal Signal Integrators In Synchronous Reference Frame," Ieee Trans. Power. Electron., Vol. 20, No. 6, Pp. 1402–1412, Nov. 2005.
- [8]. T.-L. Lee And P.-T. Cheng, "Design Of A New Cooperative Harmonic Filtering Strategy For Distributed Generation Interface Converters In An Islanding Network," Ieee Trans. Power Electron., Vol. 22, No. 5, Pp. 1919–1927, Sep. 2007.
- [9]. B. Han, B. Bae, H. Kim, And S. Baek, "Combined Operation Of Unified Power-Quality Conditioner With Distributed Generation," Ieee Trans. Power Del., Vol. 21, No. 1, Pp. 330–338, Mar. 2003.
- [10]. M. Cirrincione, M. Pucci, And G. Vitale, "A Single-Phase Dg Generation Unit With Shunt Active Power Filter Capability By Adaptive Neural Filtering," Ieee Trans. Ind. Electron, Vol. 55, No. 5, Pp. 2093–2010, May 2008.
- [11]. R. I. Bojoi, L. R. Limongi, D. Roiu, And A. Tenconi, "Enhanced Power Quality Control Strategy For Single-Phase Inverters In Distributed Generation Systems," Ieee Trans. Power Electron., Vol. 26, No. 3, Pp. 798–806, Mar. 2011.
- [12]. J. He, Y. W. Li, And S. Munir, "A Flexible Harmonic Control Approach Through Voltage Controlled Dg-Grid Interfacing Converters," Ieee Trans. Ind. Electron., Vol. 59, No. 1, Pp. 444–455, Jan. 2012.
- [13]. B. P. Mcgrath, D. G. Holmes, And J. J. H. Galloway, "Power Converter Line Synchronization Using A Discrete Fourier Transform (Dft) Based Variable Sample Rate," Ieee Trans. Power Electron., Vol. 20, No. 4, Pp. 877–884, Apr. 2005.
- [14]. H. Akagi, Y. Kanazawa, And A. Nabae, "Instantaneous Reactive Power Compensation Comprising Switching Devices Without Energy Storage Components," Ieee Trans. Ind. Appl., Vol. 20, No. 3, Pp. 625–630, Mar/Apr. 1984.
- [15]. P. Rodriguez, A. Luna, I. Candela, R. Mujal, R. Teodorescu, And F. Blaabjerg, "Multiresonant Frequency-Locked Loop For Grid Synchronization Of Power Converters Under Distorted Grid Conditions," Ieee Trans. Ind. Electron., Vol. 58, No. 1, Pp. 127–138, Jan. 2011.

### Authors Profile



Panchadarla Lavanya was born in Visakhapatnam, India in 1988. I received the B.Tech degree in Electronics & Electrical Engineering from Snakelike Vidhya Perished Engineering College, Andhra University Visakhapatnam, India in 2012 and I am currently pursuing the M.Tech degree in Power & Industrial Drives at Dadi Institute of Engineering & Technology, JNTU Kakinada, India. My interests include power quality, power-electronic devices, industrial drives and power electronics applications in distribution systems, AC & DC drives.



Mr. K. VIJAY KUMAR received B.TECH degree from Nagpur University, and M.E. (Power System Engg) From Sambalpur University, Orissa. At present he is Pursing (Ph.D) in Andhra University, Visakhapatnam. Having twenty years of experience in the field of education & taught various engineering subjects & labs in the EEE department.