

## Study of Filters through Programmable Instruments Implementing LabVIEW

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**Abstract :** The present work describes the detailed study of the active filters and its characteristics by interfacing the programmable instruments with personal computer(PC) using LabVIEW. The programmable instruments used in the present work are function generator from Protek and HP digital multimeter. Two serial ports are used to establish the communication between function generator, digital multimeter and PC through the virtual instrument (VI) developed using LabVIEW software tool. Programmable function generator produces the continuous frequencies to one of the filter circuit using LabVIEW program through one serial communication port and the corresponding output voltage of the filters along with the input frequency is stored in a file by reading the output AC voltage from the digital multimeter through another serial communication port using LabVIEW programming. The work provides the efficient study of first and second order Active filters with high precision.

**Keywords:** Digital Multimeter, LabVIEW, PC, Programmable Function Generator, Virtual Instrument.

### I. INTRODUCTION

An electric filter is a frequency selective circuit that either allows a specified band of frequencies or attenuates particular signal of frequencies [1]. Filter circuit can be designed using passive elements (resistor, capacitor, inductor) called Passive filter and also the combination of active and passive elements known as Active filters. Active filters are more economical than passive filters, easy to tune, does not cause loading of the source. Filters are most extensively used in the field of communications, signal processing and commonly used in sophisticated electronic systems like radio, TV, radar, space satellites and biomedical instrumentation [1].

In practice it creates some difficulty to study the filter characteristics by continuously varying of frequency manually and measuring the corresponding voltage. To overcome this problem the present work was aimed to design an integrated instrumentation system which consists of programmable instruments for the practical purpose that are used in labs.

The programmable instruments are meant to use in the PC based instrumentation. The former instrumentation techniques are used to interface the programmable instruments with the PC through the programmable languages which doesn't provide Graphical User Interface (GUI) to observe and to feed the input values. The programmable languages C, BASIC, etc. are used to provide the Standard Commands for the Programmable Instruments (SCPI). SCPI written in programming languages are very difficult to analyze and to modify the code according to the application requirement.

In the present work the language used to program the instruments is graphical programming language which provides a GUI to give the input values for programmable instruments. A programmable instrument with concurrent programming, Graphical user interface, and real time systems integrates in the current work [7]. The programmable instrument i.e. digital synthesis arbitrary function generator/counter (Protek 9305) and HP digital multimeter (Hewlett Packard 34401A) were used in the present system. The applications of these instruments are most extensively functional in industries and research. By implementing set of graphical programming sub routines under LabVIEW environment, one can enhance the full-fledged system in order to monitor the flow of data continuously through the interface bus using developed integrated software. Programmable function generator and HP digital multimeter were connected to two comports to develop serial communication for writing frequency to the function generator and reading AC voltage from the Digital multimeter using LabVIEW programming. In this system, programmable function generator and a digital multimeter were connected in a closed loop through personal computer.

## II. INSTRUMENTATION

### 2.1 Hardware Design

The hardware design of the present work describes the interfacing of the function generator, digital multimeter and filter circuits with PC. PC used in the present work is DELL OptiPlex 380, which comes with only one serial port and the second serial port is established with one of the USB ports. The default serial port was used to interface the digital multimeter and the serial port through the USB port was used to interface the function generator. A USB to RS232 converter was used to convert the USB port to serial port along with the driver.

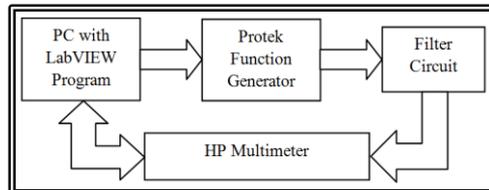


Fig. 1. Block Diagram of the Instrumentation System

Figure 1 shows the block diagram of the instrumentation system. The block diagram shows that the interface was done in closed loop fashion. The USB serial port was used to interface the protek function generator which uses only three lines from the serial port i.e. pin2 and pin3 of DB9 connector at PC end were connected to the pin3 and pin2 of the function generator end respectively for transmitting the commands. Pin5 of both PC and instrument was shorted as it acts as common ground and the rest of the pins are NCs [2].

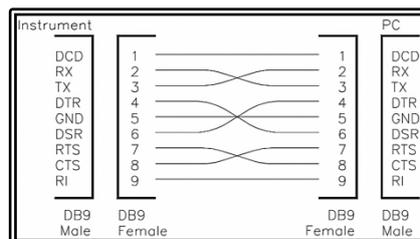


Fig. 2. Pin Configuration of PC and Digital Multimeter

Figure 2 shows the pin configuration of interfacing of the digital multimeter with PC. Pins 2, 3, and 5 was connected same as the function generator. Pins 1 and 9 were connected to the same pins in the both ends. Pins 4, 5, 7 and 8 at instrument end were connected to the pins 5, 4, 8 and 7 at PC end respectively [3]. For both instruments, front panel was used to connect it with the filter circuits. In the present study function generator is used as an input instrument that is programmed to give continuous incremental frequency with constant peak to peak voltage. The digital multimeter is the output instrument that is programmed and configured to read the AC voltage.

The present work studies the characteristics of low pass, low pass (1<sup>st</sup> order), high pass, high pass (1<sup>st</sup> order), band pass and band reject filters. Figure 3 shows the schematic circuit diagram of active low pass filter. The low pass active filter is designed by connecting an inverting amplifier to the basic RC low pass filter. The inverting amplifier is connected with unity gain. The operational amplifier OP07 was used which works with +12V and -12V. All the resistors values are 1K and capacitor value is 0.01 $\mu$ F. The AC signal from the function generator is connected to the input pins 1 and 2 of the circuit [3].

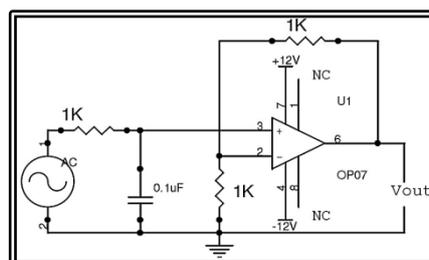


Fig. 3. Schematic Circuit Diagram of Active Low Pass Filter

PC is connected to the function generator through USB serial port to feed the continuous frequency signal to the filter circuit and the low pass filter circuit allows the low frequencies and rejects the high

frequencies. The output AC voltage is measured using digital multimeter and stored in a file. The next section discusses about the software design of the present study.

## 2.2 Software Design

The software design describes the implementation of LabVIEW to study the filter characteristics. LabVIEW is a software tool that uses graphical programming language for data acquisition and instrument control. The flexibility to analyze instrumentation made it to use in the present study [5]. LabVIEW includes conventional program development tools, so that user can set breakpoints, animate program execution to see how data passes and single-step through the program to make debugging and program development easier. LabVIEW contains application-specific libraries for general purpose interface bus (GPIB), serial instrument control, data analysis, data presentation and data storage [5]. Depending up on the application, external interfaces are integrated with LabVIEW. External interfaces like function generator, digital oscilloscope and digital multimeter are used for data acquisition through serial communication port.

In the present work complete software is developed in LabVIEW for function generator and digital multimeter. Figure 4 and 5 shows the front panel and block diagram of the LabVIEW VI of the present work respectively.

Front panel is used as GUI and to configure the settings for serial port operation. *VISA Resource Name* and *VISA Resource Name 2* are VIs which were used to select the com port in the available com port list. *Baud rate*, *data bits*, *parity*, *stop bits* and *flow control* are 32 bit unsigned long integer data types which are used to configure the serial port. The *input frequency* and *number of iteration* are 32 bit integer data types which are used to give the input frequency, and the count increment value and number of iterations for the *for loop*. The cluster *Serial Configuration* is used to set the values that are required for serial communication like *data bits*, *baud rate* and *parity*. *Function (0: DC Voltage)* is 32 bit integer data type which is used to select the output type of the digital multimeter. *Write data* is a string indicator which shows the input frequency that is feeding to the function generator. *Measurement* is a 64 bit double data type which is used to show the measured AC voltage on the front panel.

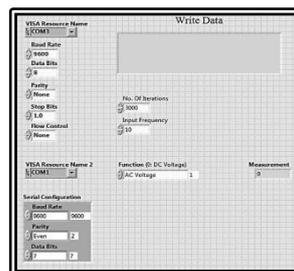


Fig. 4. Front Panel of LabVIEW VI

The block diagram adds the code in graphical representation of functions to control the front panel objects. The block diagram contains the graphical source code, also known as G code or block diagram code. Front panel objects appear as terminals on the block diagram. Figure 5 shows the block diagram which the G code for the present work. The functions used in the present study to write the data to the function generator are *VISA Configure Serial Port VI*, *VISA Write Function, Property Node*, *VISA Close Function*, *Number to Decimal String Function* and *Concatenate String Function*. *VISA Configure Serial Port VI* initializes the serial port specified by *VISA resource name* to the specified baud rate, data bits, parity, stop bits and flow control settings. *VISA Write Function* writes the string of commands from *Input Frequency* and from two constants from the interface specified by *VISA resource name*.

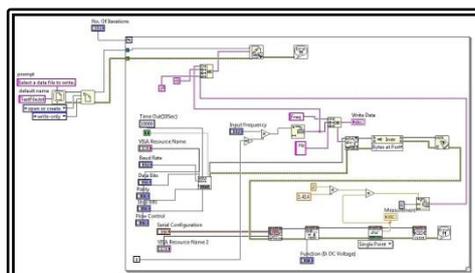


Fig. 5. Block Diagram of LabVIEW VI

The *Property Node* adapts to the class of the objects that was referred by the user. Property nodes are preconfigured in LabVIEW library to access XML Properties, VISA properties, .NET properties, and ActiveX



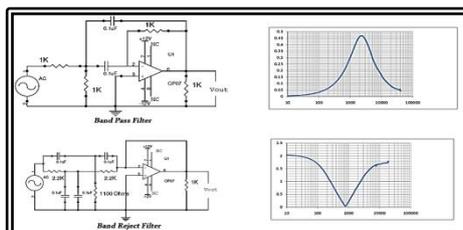


Fig. 8. Band Pass Filter, Band Reject Filter and Response Curves

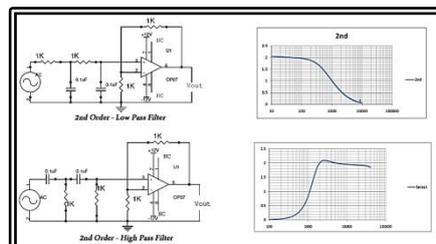


Fig. 9. 2<sup>nd</sup> Order Low Pass Filter, 2<sup>nd</sup> Order High Pass Filter and Response Curves

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