# Thermal Annealing of CdMnTe Material Being Developed for Nuclear Radiation Detection Applications

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**Abstract:** Cadmium manganese telluride (CdMnTe) is one of the semiconductor materials with potential applications at room-temperature for nuclear and radiological detection. CdMnTe crystals grown by Bridgman technique are prone to tellurium inclusions and related defects that limit their performance as X-rays and gamma-rays detectors. The major reason for this is that they are grown in a tellurium-rich environment. These defects could trap charges that are generated by X-rays and gamma rays thereby degrading the charge transport properties of the detectors and reducing their carrier lifetime. This in turn leads to poor performance by the detector. One of the solutions to this problem is post-growth thermal annealing. In this paper we present experimental results of annealing a CdMnTe wafer at 720 °C and in cadmium vapor. The CdMnTe wafer and cadmium were sealed in a quartz ampoule at a vacuum of 10<sup>-5</sup> mbar. We used a three-zone furnace that enabled us to adjust the three heating elements to get a flat region of 720 °C in the temperature profile where the wafer was annealed. Infrared transmission microscopy showed changes to the sizes and positions of the tellurium inclusions. There are reductions in the dimensions of the medium-size Te inclusions. Some Te inclusions were completely eliminated while others broke up to form much smaller inclusions. Current-voltage measurements showed that the resistivity of the CdMnTe wafer was reduced by 71 %, from 2.44 x 10<sup>5</sup>  $\Omega$ -cm to 7.17 x 10<sup>4</sup>  $\Omega$ -cm after annealing in Cd vapor.

Keywords: CdMnTe, defects, gamma-rays, nuclear and radiological detection, thermal annealing, X-rays.

## I. Introduction

Room-temperature semiconductor nuclear detectors such as cadmium telluride (CdTe) and cadmium zinc telluride (CdZnTe) could be operated without cryogenic cooling. This gives them the advantages of reduced operation cost and fabrication of portable and hand-held devices for the detection of X-rays and gamma-rays. CdTe and CdZnTe are commercially available [1]-[4], but the cost of their production is very high due to low growth yields of crystals with a high-resistivity, less defects and high-intrinsic efficiency. Recently, researchers have been investigating cadmium manganese telluride (CdMnTe) for room-temperature nuclear and radiological detection applications [5]-[10]. The main advantage of growing CdMnTe crystal is that the segregation coefficient of Mn in cadmium telluride (CdTe) matrix is about 1.0, compared to 1.35 for Zn in the case of CdZnTe [9]. This is expected to result in the growth of CdMnTe with a uniform Mn content, which potentially would give a higher homogeneity and yield of detector-grade crystals [9], [10]. CdMnTe also has a wide energy band gap (1.7 to 2.2 eV), giving it a wide range of tenability, due to the compositional effect of Mn that increases it by ~13 meV per atomic percent of Mn, compared to 6.7 meV for Zn [10]-[12].

A major problem with CdMnTe crystals, also common to CdZnTe [13], is the presence of Te inclusions and related defects that limit their ability to detect X-rays and gamma-rays. In CdMnTe crystals grown by the Bridgman technique, the Te inclusions are formed from the evaporation of Cd and decomposition of segregated Te from the decomposition of the melt. One method of removing these performance-limiting Te inclusions is post-growth thermal annealing. There are two primary approaches to post-growth thermal annealing [14]: 1) annealing the crystal at a *constant temperature* (around or above the melting point of Te) in a Cd vapor, and 2) annealing under a *temperature gradient* to induce the migration of Te particles leaves regions with fewer and smaller Te inclusions, which can be harvested for detector fabrication. In constant temperature annealing in Cd-rich atmosphere, the Cd diffuses into the CdMnTe matrix where it combines with excess Te to form crystalline material. In the present work, we investigate how annealing in Cd vapor affects Te inclusions and the electrical resistivity of CdMnTe crystals.

# **II.** Experiment

CdMnTe crystal grown by Bridgman technique was cut from the ingot. The size of the CdMnTe wafer used in this experiment is 4.5 mm x 4.0 mm x 1.5 mm as measured using a digital Vernier caliper. The CdMnTe sample has medium resistivity of the order of  $10^5 \Omega$ -cm (a detector-grade CdMnTe wafer is expected to have resistivity of the order of  $10^{12} \Omega$ -cm). The sample was mechanically polished with 800-grit and 1200-grit silicon carbide abrasive papers. This was followed by polishing on multi-tex pads with alumina powder of decreasing size from 3 microns to 0.1 micron. Infrared (IR) transmission microscopy was used to image the size and location of Te inclusions before and after annealing using AmScope Microscope. The AmScope Camera MD900E was adjusted to record IR images. The CdMnTe matrix is transparent to IR light while the Te inclusions are opaque and thus their images are recorded.

Current-voltage (I-V) measurements were carried out to study the effect of thermal annealing on the electrical resistivity of the CdMnTe sample. Before annealing, electrical contacts were deposited on the sample by depositing a gold chloride (AuCl<sub>3</sub>) solution on the center of the two opposite planar surfaces of the CdZnTe wafer (see Fig. 1) using the electroless pipette method. After leaving the AuCl<sub>3</sub> solution on a planar surface for about to 30 seconds, the excess solution was removed by blotting it out using the tip of a clean wiping cloth. The bulk leakage current for every applied voltage were measured for each sample using a Keithley Picoammeter/Voltage Source. The electrical contacts were removed before annealing and the samples polished in with alumina powder.



Fig. 1. CdMnTe wafer showing of the two circular-shaped gold contacts.

To anneal the sample, it was sealed in a quartz ampoule under a vacuum of  $10^{-5}$  mbar using the vacuum pump shown on the right-side of Fig. 2. The CdMnTe wafer was placed at one end of the ampoule and Cd was placed at the opposite end with a constriction between them. The three-zone furnace, shown on the left-side of Fig. 1, was used for annealing the samples. The heating temperatures of the heating elements were adjusted to get the desired temperature profile of the furnace to meet the desired annealing conditions. The temperature profile, shown in Fig. 3, was obtained by using a thermocouple placed and moved along the central axis of the cylindrical of the annealing furnace.



Fig. 2. Three-zone annealing furnace (shown on left-side) and vacuum pump (shown on right-side) for removing when sealing CdMnTe wafers in ampoules.

The three heating elements of the furnace enabled us to get constant temperature region of 720  $^{\circ}$ C where the sealed ampoule with the sample and Cd were placed. The sample was annealed for 12 hours. The polishing process, infrared transmission microscopy, electrical contacts deposition, and current-voltage measurements were repeated after annealing the sample.



Fig. 3. The temperature profile of the furnace preset for annealing the CdMnTe samples.

# III. Results and Discussions

The infrared images of the same region of the CdMnTe wafer before and after annealing in Cd vapor are shown in Fig. 4. There are changes in the shapes and sizes of the Te inclusions. The change in shape is more noticeable in the bigger Te inclusions. There is reduction in the dimension of the medium-size Te inclusion, located in the central right region. Some of the smaller inclusions in the upper side of the image have been eliminated while other were replaced with much smaller inclusions.



(a) Before annealing  $(256 \ \mu m \ x \ 367 \ \mu m)$  (b) After annealing  $(256 \ \mu m \ x \ 367 \ \mu m)$ **Fig. 4.** Infrared images of the same region of the CdMnTe wafer before and after annealing in Cd vapor.

The change in shape, reduction in size, and elimination could be attributed to two major processes [14], viz. reaction between Te inclusions and Cd that diffused into the CdMnTe matrix, and diffusion of Te within the matrix. In the present work, we also observe a group of tiny Te inclusion that appeared in the CdMnTe wafer after annealing. This could be from Te particles diffusing from Te inclusions that were originally present in that region before annealing.

The current-voltage curves of the CdMnTe wafer before and after annealing are shown in Fig. 5. The resistivity  $\rho$  of the material was calculated as

$$\rho = \frac{RA}{l} \tag{1}$$

where *R* is electrical resistance, *A* is contact area, and *l* is the thickness of the CdMnTe wafer. Since V = IR, the resistance is obtained from the inverse of the slope on the linear portion of the I-V curves in Fig. 5 as

$$R = \frac{\Delta V}{\Delta I} = \frac{1}{slope} \tag{2}$$

From Eq. (1) and the slopes in Fig. 5 the resistivity of the CdMnTe wafer before annealing was found to be 2.44 x  $10^5 \Omega$ -cm, and the resistivity after annealing in Cd vapor is 7.17 x  $10^4 \Omega$ -cm (the *slopes* were obtained using points corresponding to 21 V and 40 V). This is a 71% reduction in the resistivity of the CdMnTe wafer. This is close to a reduction of one order of magnitude in the resistivity. In CdZnTe annealed at 670 °C for 50 hours in Cd vapor, it was reported that the resistivity of the material was reduced up to five orders of magnitude [15]. The reasons for these observations are changes caused by the annealing processes to the concentrations of deep-level Te anti-sites, Cd interstitials, and Cd vacancies, that are responsible for the high resistivity of CdZnTe [15]-[17], and in CdMnTe. For CdZnTe annealed in Cd vapor, it was suggested by Yang et al. [13] that the electrical compensation was lost due to extra Cd interstitials from the Cd vapor, thus resulting in shifting of the Fermi level towards the bottom of the conduction band. The same explanation could be applied to CdMnTe annealed in Cd vapor in this study.



Fig. 5. Current-voltage curves of the CdMnTe wafer before and after annealing in Cd vapor.

### IV. Conclusions

We have presented experimental results of annealing a CdMnTe wafer, at 720  $^{\circ}$ C and a vacuum of 10<sup>-5</sup> mbar, in cadmium vapor. We used a three-zone furnace with three heating elements that enabled the adjustment of the temperature profile to get a flat region of 720  $^{\circ}$ C where the CdMnTe wafer was annealed, in a sealed quartz ampoule. Infrared transmission microscopy showed reduction in the dimension of the medium-size Te inclusions. Some Te inclusions were completely eliminated while others broke up to form much smaller inclusions.

The current-voltage measurements indicated that the resistivity of the CdMnTe crystal was reduced by 71 %, from 2.44 x  $10^5 \Omega$ -cm to 7.17 x  $10^4 \Omega$ -cm after annealing in Cd vapor. Detector-grade CdMnTe and CdZnTe wafers are expected to have resistivity of the order of  $10^{12} \Omega$ -cm. The resistivity of CdMnTe crystals could be increased during crystal growth by doping the material with indium [1]. The problem of reduction in resistivity after annealing CdZnTe in Cd vapor could be resolved by annealing in a combination of Cd-rich and Zn-rich atmosphere [15]. Thus, it will be worth investigating the annealing of CdMnTe wafers in a combination of Cd and Mn vapors. The reduction in the resistivity of the CdMnTe wafer annealed in this study for 12 hours at 720 °C is within one order of magnitude. Reduction in resistivity of CdZnTe annealed at 670 °C for 50 hours in Cd vapor was reported to be up to five orders of magnitude. Thus, annealing for a shorter period (12 hours in this study) could be an advantage when compared to longer annealing periods.

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