Patch Antenna Array Fault Modeling and Its Monitoring

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Abstract : The On/Off faults are among the most prominent hazards observed in antenna arrays. They behave like a catalyst towards steady degradation of the circuit's performance. This paper is regarding fault monitoring in the microstrip patch antenna array [1][6]. The fault modeling, included here is single and double fault models of the patch antenna array of 8 elements. There is a marked deviation in the parametric properties of the array because of On/Off faults. By observation and analysis of these trends, the detection of fault in the array is simplified to a great extent. We aim at identification of On/Off faults in a faulty array structure by observation of its characteristic properties (like S- parameters and Radiation Pattern) and their analysis using Matrix Comparison Method and Scaled Conjugate Gradient Back Propagation Algorithm in Artificial Neural Network.

Keywords: Patch antenna, patch array antenna feed, S-parameter, radiation pattern, characteristic impedance, neural network.

I. INTRODUCTION

Microstrip array [1],[2] is designed at 2.49 GHz used in IEEE 802.11b high-speed TCP/IP communications. It can be used as mass-produced, inexpensive equipment designed for FCC part 15 unlicensed uses. Microstrip antennas have been one of the most innovative topics in antenna theory and design in recent years, and are increasingly finding application in a wide range of modern microwave systems. The development of microstrip antennas has been driven by systems requirements for antennas with low-profile, low-weight, low-cost, easy integrability into arrays or with microwave integrated circuits, or polarization diversity. One of the best features of microstrip antennas is the ease with which they can be formed into arrays, and a wide variety of series-fed, corporate-fed, scanning, and polarization-agile arrays have been designed using microstrip elements. With the help of IE3D simulation tool, which is most versatile, easy to use, efficient and accurate electromagnetic simulation of Matrix Comparison Method [13] and Scaled Conjugate Gradient Back Propagation Algorithm in Artificial Neural Network [9], the MATLAB software is used.

II. Design Of Single Patch

The required specifications for the patch [9] are: *Resonant Frequency* $(f_0) = 2.49 \text{ GHz}$

Dielectric constant of Substrate=2.33

Height of the substrate =1.6mm

Matching Load= 50Ω

Using the standard formulae and the standard design considerations we obtain a single patch with following parameters:

Width of the patch (W)=46.66 mm Effective Dielectric (C_{reff})=2.2248 Effective Length (Leff)=40.40 mm Length extension (ΔL)=0.8175 mm



Length of the patch (L)=38.765 mmInset position (y)=13.665 mmInset width (W₀)=5.2578 mm

Fig. 1-schematic of single patch element with inset line feed



Fig. 2- The reflection loss is around -37.5 dB indicating effective coupling between port and element. The microstrip inset feed line (figure 1) is a conducting strip, usually of much smaller width compared to the patch. It's easy to fabricate, simple to match by controlling the position of inset [3][4] and rather simple to model. But with increase in substrate thickness, spurious and surface radiation increase resulting in limiting the bandwidth.



Fig. 3- This graph gives us the 2D view of radiation pattern in the azimuthal plane. The half power beamwidth is determined to be 74.42°.

We determine the physical spacing between the elements and the structure of the feed using the above information. To have zero grating lobes we kept the inter element spacing to be $\lambda/2$. Since any larger distance resulted in one or more grating lobes while smaller distances led to introduction of inherent asymmetries into the design. The feed length was taken to be approximately $\lambda/4$ for impedance transformation at the desired resonant frequency of 2.49GHz. The feed width was chosen on a hit-and-trial basis to obtain the required load matching of 50 Ω . The initial width of the feed connecting the single element to the entire feed structure was taken to be the same as inset width (W₀).

III. Antenna Array Fault Modeling

The antenna array feed is based on parallel feed technique with improved beam shape [7]. The resonant frequency has changed from 2.49 GHz to 2.5 GHz (figure-2). This much of shift is tolerable since the maximum limit of tolerance is 2%. The shift in our case is 0.43%. As we can see that at resonant frequency we have a dip of -37.5 dB which gives very good antenna performance (figure 5). Here we have considered only the single and double fault modelling with different combinations of faults in the elements of the patch antenna array. The deviation in values of reflection coefficient and radiation pattern from those of fault-free case is valid. At the resonance frequency, these deviations are reduced. Hence, the patch array is very less prone to single fault errors. In case of double faults, these deviations are much higher than those of single fault cases. At resonance frequency, these deviations got reduced. Although the deviations are within tolerable limit, they are still traceable.



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III a. The single fault

In the following figures, the fault model is shown with feed discontinuity fault in the first element and the antenna characteristics parameter.



Fig. 7- current distribution in single fault





The point here is to be noted that patch with discontinued feeding is still showing current distribution, which is the parasitic effect [8] of the elements nearby.

III b. The double fault

In the following figures, the fault model is shown with feed discontinuity fault in the first and second element and the antenna characteristics parameter.



combinations of (1,3), (1,4) and (1,5) faults

In the following figures antenna characteristics for the fault of combinations of (1, 3), (1, 4) and (1, 5) are only shown (figure 12,13). Rest of the cases can similarly be modelled.



Figure 13-2D polar plot of Double Faulty cases for combinations of a(1,3),b (1,4) andc (1,5) faults

These are the 2D polar plots for each of the double faulty cases, which shows gain for double fault cases of 13.35 dBs.

IV. Antenna Array Fault Monitoring

IV a. Matrix Comparison Method

The efficiency of the matrix comparison method [13] for the fault identification is up to 85 % on an average for up to double fault in array.

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Fig.14- simulation of matrix comparison method with MATLAB

IV b. Artificial Neural Network Method

The ANN is based on Scaled Conjugate Gradient Back Propagation Algorithm in Artificial Neural Network [14][15], gives an accuracy of more than 95 % and more for most of the test cases.



Figure 16- fault monitoring simulation

Monitoring these deviations using ANN(figure 15) we obtain the following Neural Network training statistics: $Epochs \ elapsed = 228$

Time elapsed = $2 \min 6 \sec$ Performance achieved = 0.000964314Gradient at last = 0.0326946

V. Conclusion

The design of patch antenna for IEEE 802.11 has been successfully implemented at 2.49 GHz. The patch gives us very accurate results.

The 8 – element microstrip antenna array meets the requisite specifications. Although there is relative shift in resonant frequency (from 2.49 to 2.5 GHz), it is within tolerable limits (2%).

It is found out that on an average the Matrix Comparison Method gives an accuracy of 80 - 85 %. This method is also found to be susceptible to ambiguity decisions.

The ANN gives an accuracy of more than 95 % and more for most of the test cases. This method is free of any error generated because of ambiguity.

VI. Future Scope

Dipole antennas [12] can be used instead of microstrip patches. It is known that dipoles are more susceptible to flaws in array structures and hence errors can be easily traced. Circularly polarized patches can be used instead of linearly polarized ones. Cavity model analysis can be used instead of transmission line model.

The same analysis can be implemented for a larger array structure consisting of 32/64 elements. Higher degree of faults can be analysed in the array.

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