



IOSR Journals

International Organization
of Scientific Research

*IOSR Journal of VLSI and
Signal Processing*

e-ISSN : 2319 – 4200

Volume : 11 Issue : 1

p-ISSN : 2319 – 4197

IOSR-JVSP

Contents :

Design, Simulation, and Analysis of Different Operational Factors of a 4-bit Carry Look-Ahead Adder Circuit in Microwind at Several CMOS Technology Nodes <i>Muhibul Haque Bhuyan, Md. Akhtaruzzaman</i>	01-08
Educational Introduction to VLSI Circuit Design and Simulation with Tanner-EDA Tools <i>George P. Patsis</i>	09-21
MOSFET Compact Verilog-A Model Implementation in S-Edit <i>George P. Patsis</i>	22-28