



Managing Editor Board

- ❖ Dr. B. D. Venkataamana Reddy
Dept.of ECE, MITS, A.P, India
- ❖ Dr. Mohsin khan
Rural Education and Development Foundation, Pakistan
- ❖ Dr. S.Sasikumar
ANNA university, India
- ❖ Dr. P. Karthigaikumar
Karunya University, India
- ❖ Dr. Rania Ahmed
jazan university, Egypt
- ❖ Dr. Roukhe Hassane
Ismac, Malaysia
- ❖ Dr. Aleksandr Cariow
West Pomeranian University of Technology, Szczecin, Poland
- ❖ Dr. Swapnadip De
Jadavpur University, India

Contact Us

Website URL : www.iosrjournals.org
Email : Support@iosrmail.org



Qatar Office:

IOSR Journals
Salwa Road
Near to KFC and Aziz
Petrol Station,
DOHA, Qatar

India Office:

EHTP, National Highway
8, Block A, Sector 34,
Gurugram, Haryana
122001

Australia Office:

43, Ring Road,
Richmond Vic 3121
Australia

New York Office:

8th floor, Straight hub,
NS Road, New York,
NY 10003-9595

IOSR-JVSP

Contents :

Design, Simulation, and Analysis of Different Operational Factors of a 4-bit Carry Look-Ahead Adder Circuit in Microwind at Several CMOS Technology Nodes <i>Muhibul Haque Bhuyan, Md. Akhtaruzzaman</i>	01-10
Power reduction in SoC platform <i>Dr. V.Anandi, Dr.M.Ramesh</i>	11-17
MOSFET Compact Verilog-A Model Implementation in S-Edit <i>George P. Patsis</i>	18-24
VHDL-AMS Macro models of MOSFET. Consideration of Gate Length Variability And Single-Electron-Transistors <i>George P. Patsis</i>	25-29
Automatic Detection of Cardiac Arrhythmia Through ECG Signal Analysis: A Review <i>Gopisetty Ramesh, D. Satyanarayana, M. Sailaja</i>	30-41