



IOSR Journals

International Organization
of Scientific Research

*International Organization
of Scientific Research
Community of Researchers*

This is to confirm that

Kuldeep Singh et al.

Published following article

**Design and FPGA Performance Evaluation of an 8-Bit
Ordinary Truncated Multiplier for Approximate
Computing**

Published in IOSR Journal of VLSI and Signal Processing
Volume 16, Issue 5, (Sep. - Oct. 2026)

Impact Factor : 2.82

E-mail id : jvsp@iosrmail.org

Web : www.iosrjournals.org

IOSR-JVSP is peer reviewed refereed journal approved by UGC



Editor In Chief
IOSR-JVSP



IOSR Journals

International Organization
of Scientific Research

*International Organization
of Scientific Research
Community of Researchers*

This is to confirm that

Vinod Kumar et al.

Published following article

**Design and FPGA Performance Evaluation of an 8-Bit
Ordinary Truncated Multiplier for Approximate
Computing**

Published in IOSR Journal of VLSI and Signal Processing
Volume 16, Issue 5, (Sep. - Oct. 2026)

Impact Factor : 2.82

E-mail id : jvsp@iosrmail.org

Web : www.iosrjournals.org

IOSR-JVSP is peer reviewed refereed journal approved by UGC



Editor In Chief
IOSR-JVSP



IOSR Journals

International Organization
of Scientific Research

*International Organization
of Scientific Research
Community of Researchers*

This is to confirm that

Ajay Kumar et al.

Published following article

**Design and FPGA Performance Evaluation of an 8-Bit
Ordinary Truncated Multiplier for Approximate
Computing**

Published in IOSR Journal of VLSI and Signal Processing
Volume 16, Issue 5, (Sep. - Oct. 2026)

Impact Factor : 2.82

E-mail id : jvsp@iosrmail.org

Web : www.iosrjournals.org

IOSR-JVSP is peer reviewed refereed journal approved by UGC



Editor In Chief
IOSR-JVSP



IOSR Journals

International Organization
of Scientific Research

*International Organization
of Scientific Research
Community of Researchers*

This is to confirm that

Satbir Singh et al.

Published following article

**Design and FPGA Performance Evaluation of an 8-Bit
Ordinary Truncated Multiplier for Approximate
Computing**

Published in IOSR Journal of VLSI and Signal Processing
Volume 16, Issue 5, (Sep. - Oct. 2026)

Impact Factor : 2.82

E-mail id : jvsp@iosrmail.org

Web : www.iosrjournals.org

IOSR-JVSP is peer reviewed refereed journal approved by UGC



Editor in Chief
IOSR-JVSP